

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 108391-00011	SERIAL NO. 09/652,023
OPIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>		APPLICANT Yasuhiro WAKIMOTO	
		FILING DATE August 31, 2000	GROUP 2186



U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
Whe	AA	5,802,554	9/01/98	CACERES et al			
Whe	AB	5,845,311	12/01/98	PIGUET et al			RECEIVED
	AC						APR 16 2004
	AD						
	AE						Technology Center 2100
	AF						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO PART.
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

Whe	AM	"The 68040 On-Chip Memory Subsystem", Robin EDENFIELD et al, Computer Society International Conference, Spring Meeting, February 26, - March 2, 1990, Vol. Conf. 35, pgs. 264 - 269.
Whe	AN	"The DSP32C: AT&T's Second Generation Floating-Point Digital Signal Processor", Michael FUCCIO et al, IEEE Micro, December 1, 1988, Vol. 8, No. 6, pgs. 30 - 47.
	AO	

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EXAMINER <i>Yasuhiro Wakimoto</i>	DATE CONSIDERED 9/29/04
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	